

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Patent Application of: |) | Group Art Unit: 2811 |
|----------------------------------|---|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Shunpei YAMAZAKI |) | Examiner: S. Loke |
| Serial No. 09/433,705 🗸 |) | CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, |
| Filed: November 4, 1999 |) | |
| For: SEMICONDUCTOR DEVICE HAVING |) | |
| LDD REGIONS |) | Alexandria, VA 22313-1450, on April 19, 2005. |
| | | Rose Fichel |

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated January 19, 2005, submitted herewith are corrected formal drawings consisting of Figures 12-14, 17-19, 21 and 24-25.

Respectfully submitted,

Eric J. Robinson

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